

FIG. 1

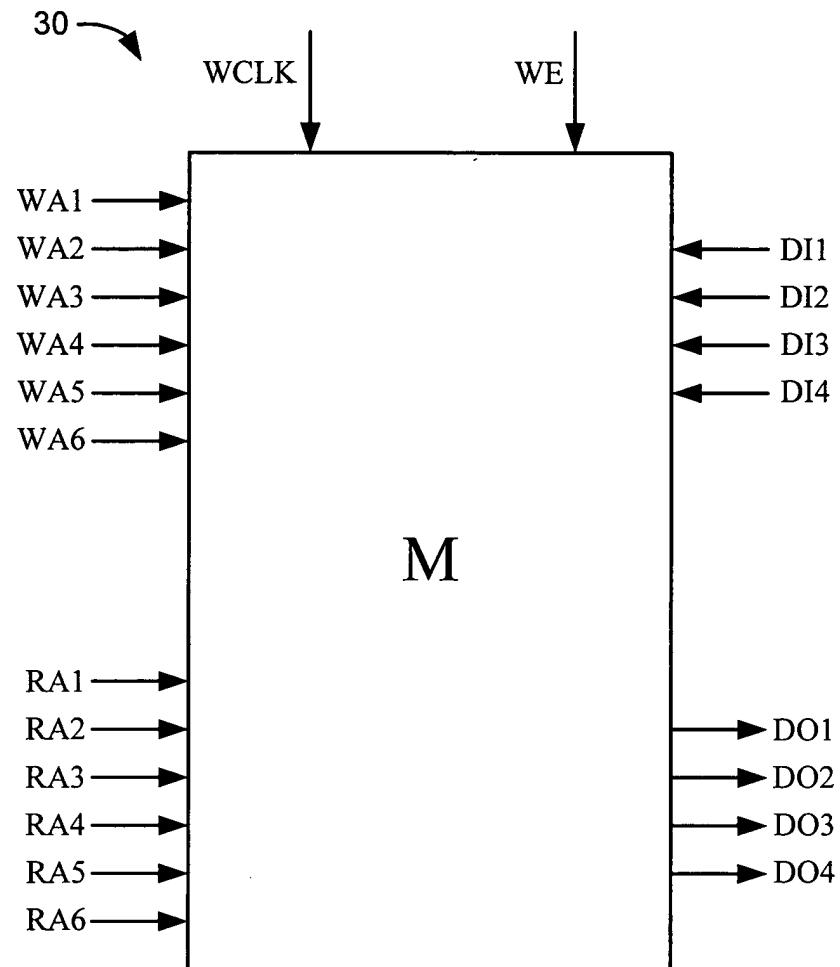


FIG. 2

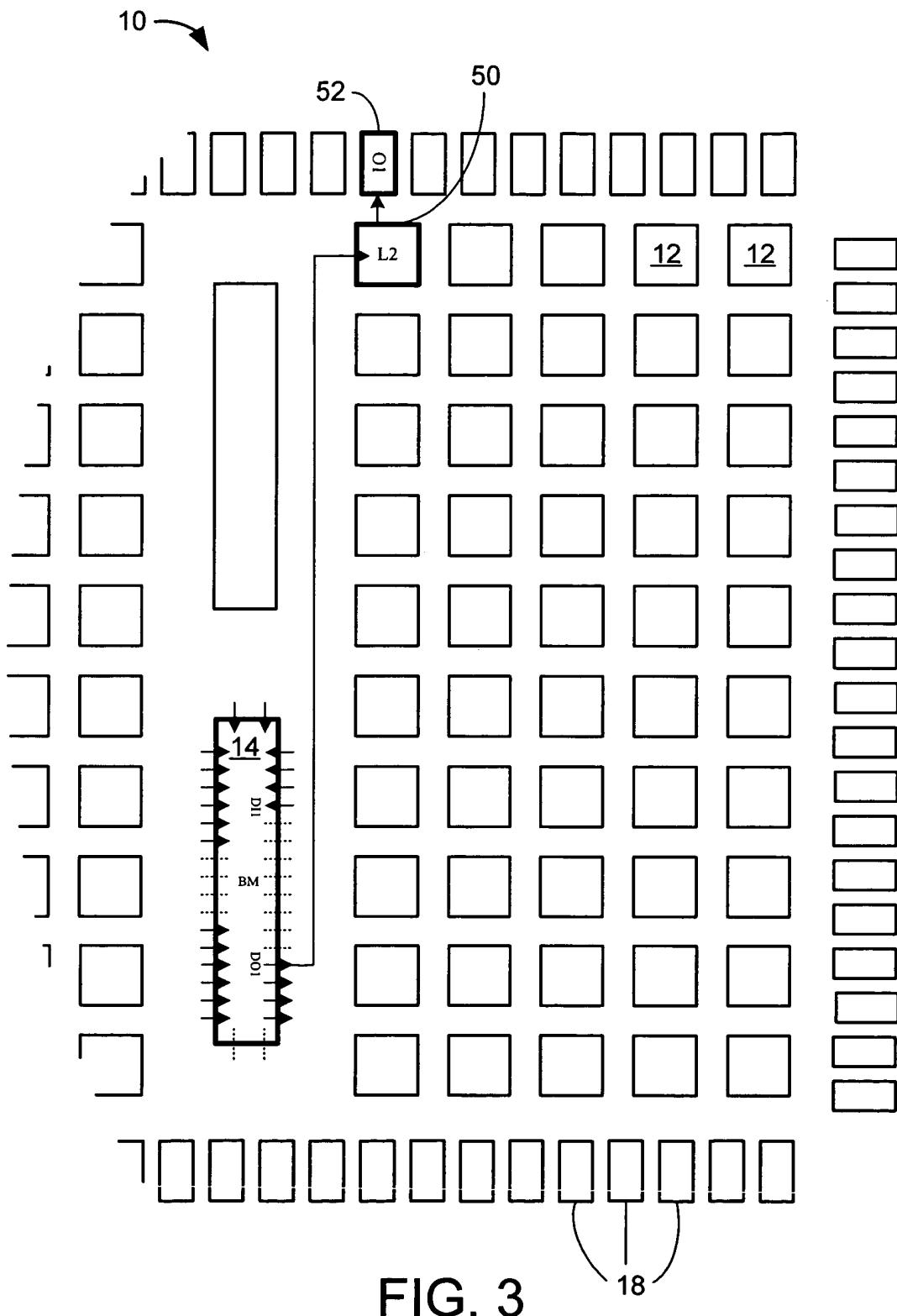


FIG. 3

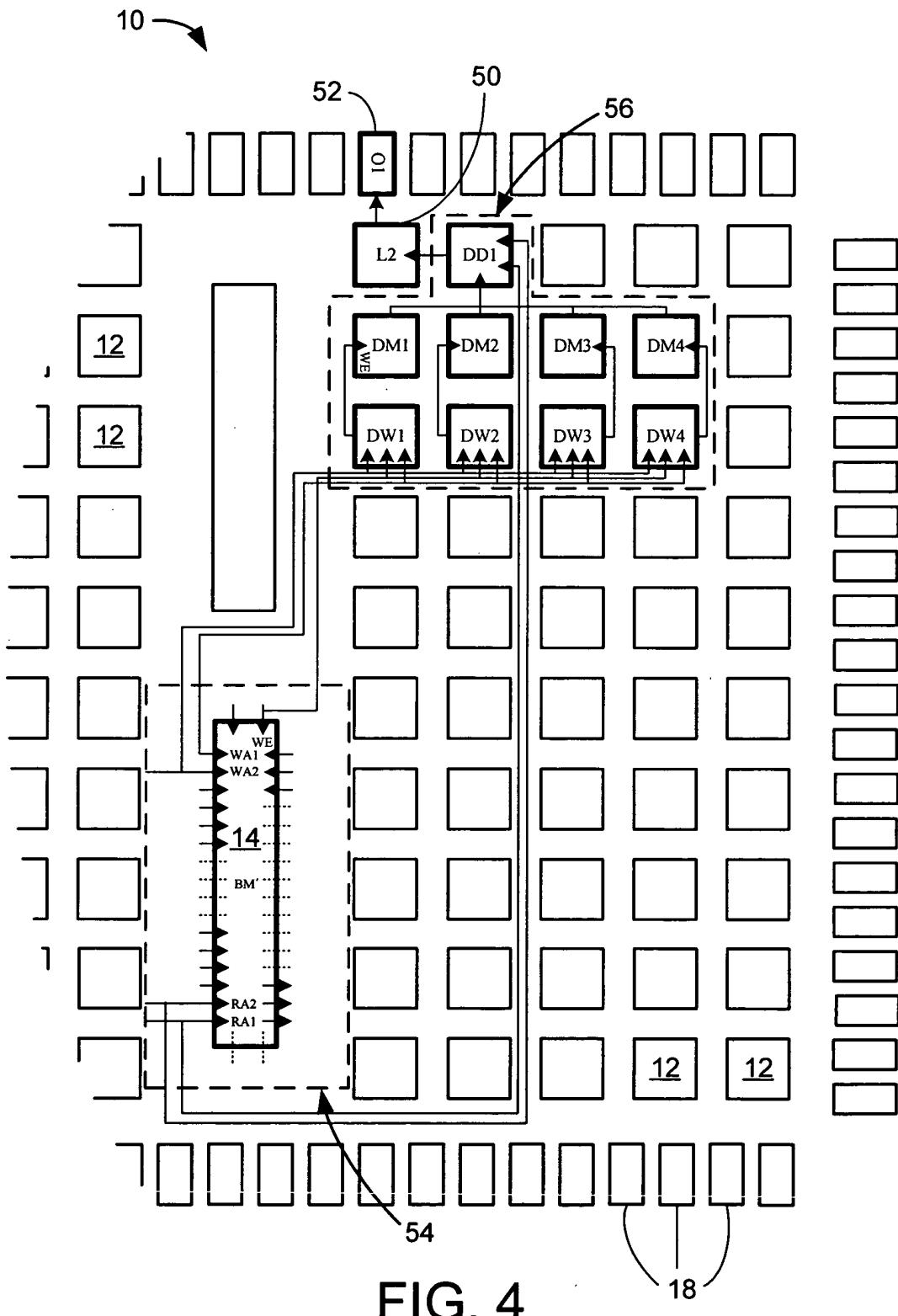


FIG. 4

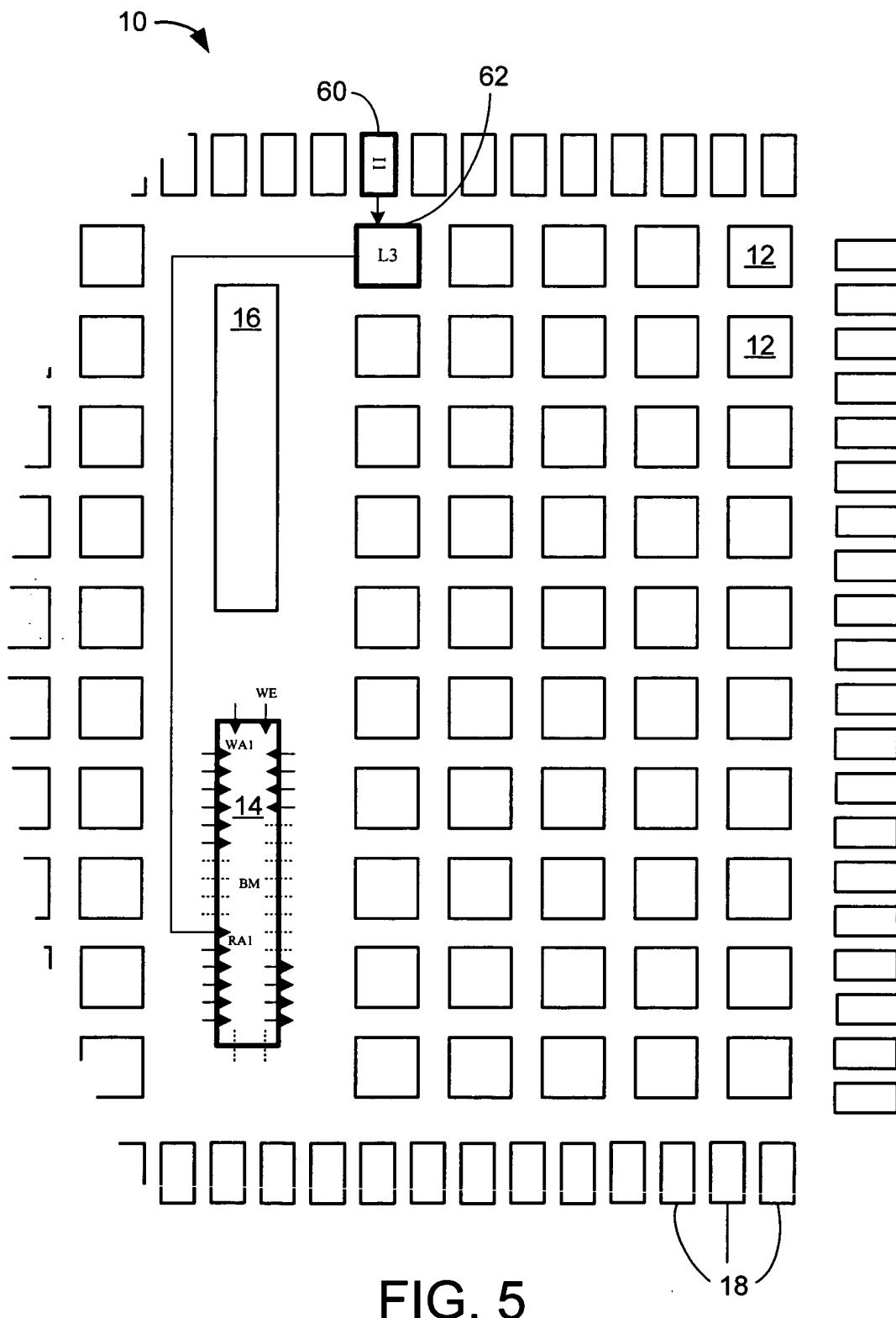


FIG. 5

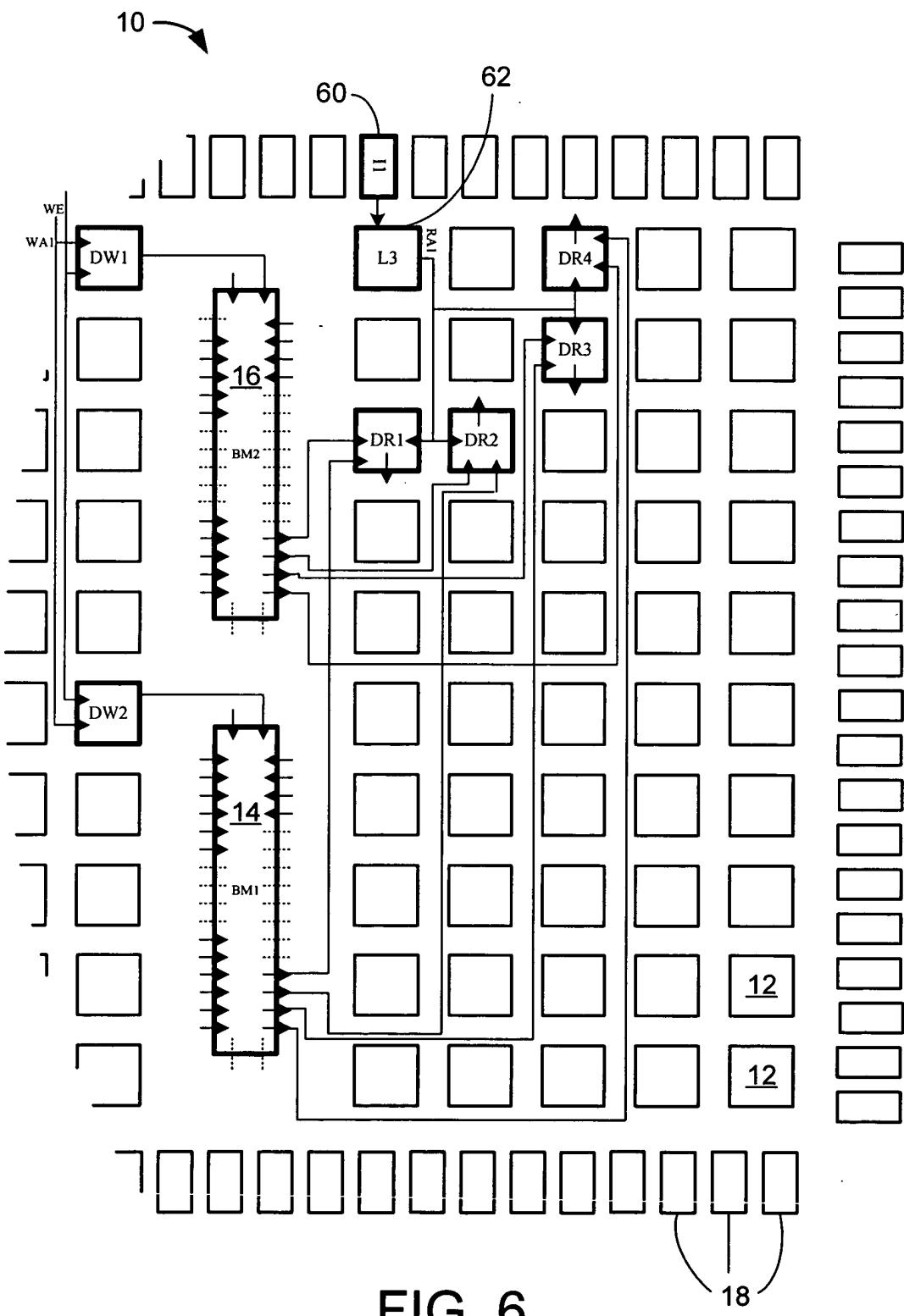


FIG. 6

For: MEMORY RE-IMPLEMENTATION FOR FIELD
PROGRAMMABLE GATE ARRAYS
Inventor(s): Suris et al.
Express Mail Label No. EV339204714US
Date of Deposit: February 23, 2004

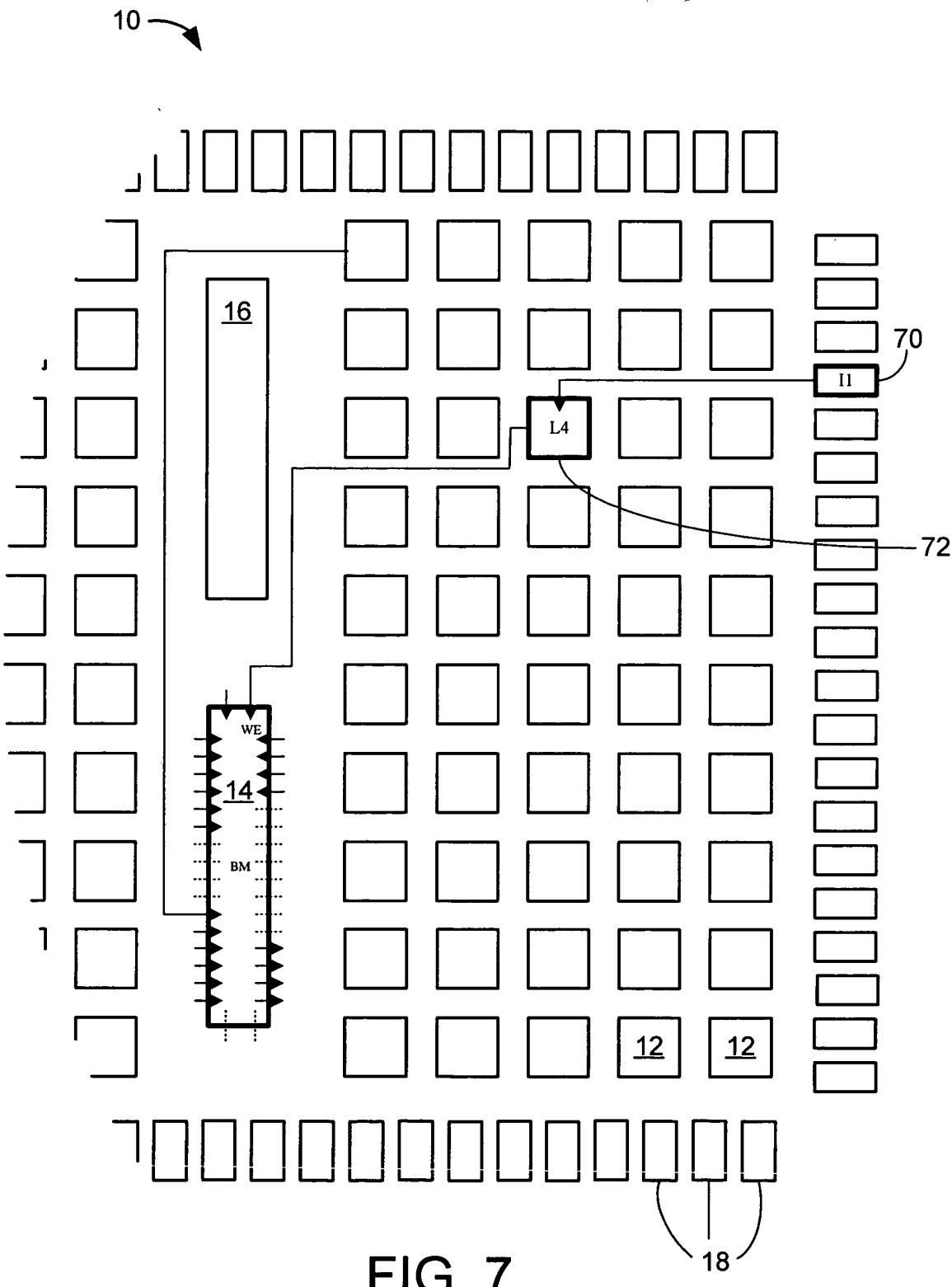


FIG. 7

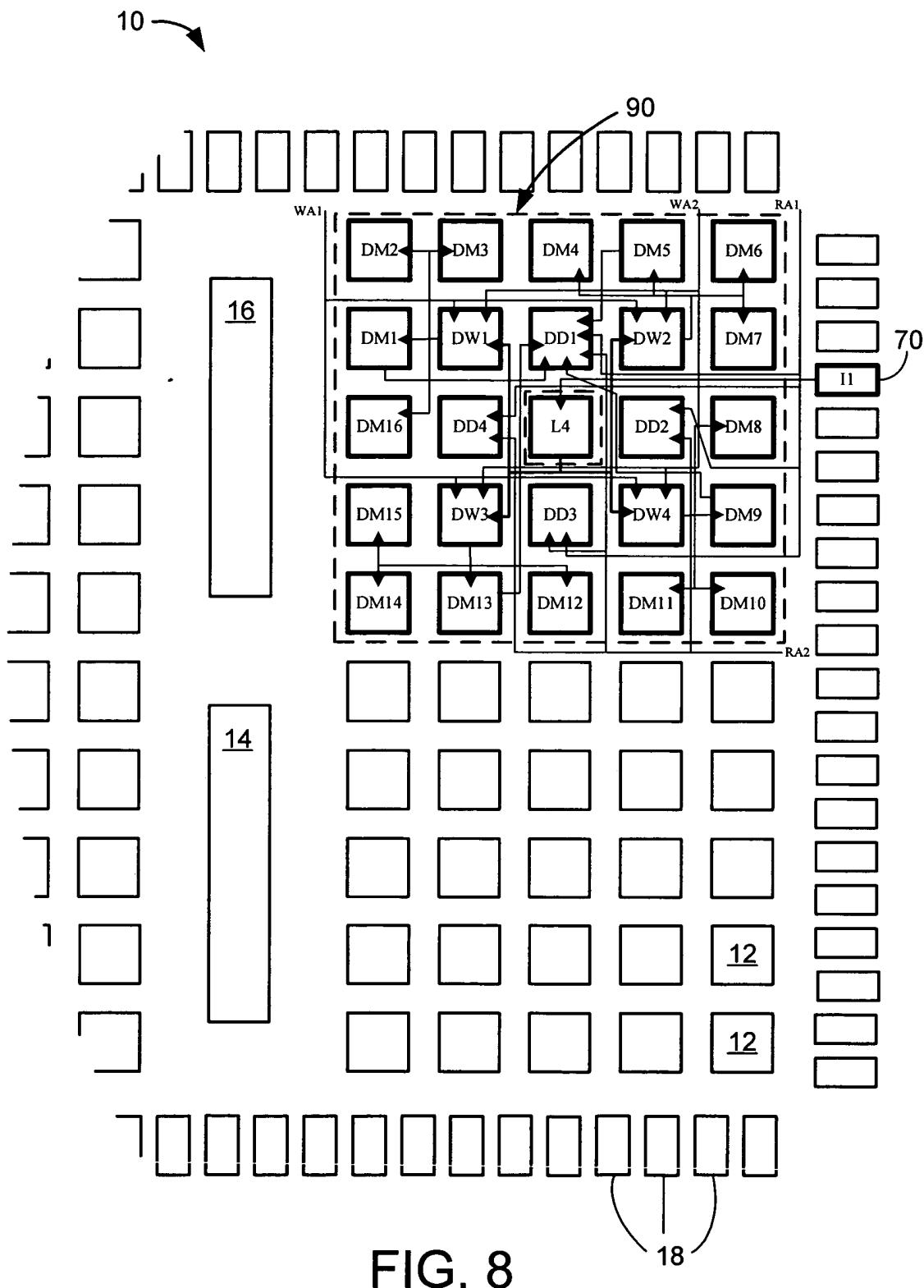


FIG. 8

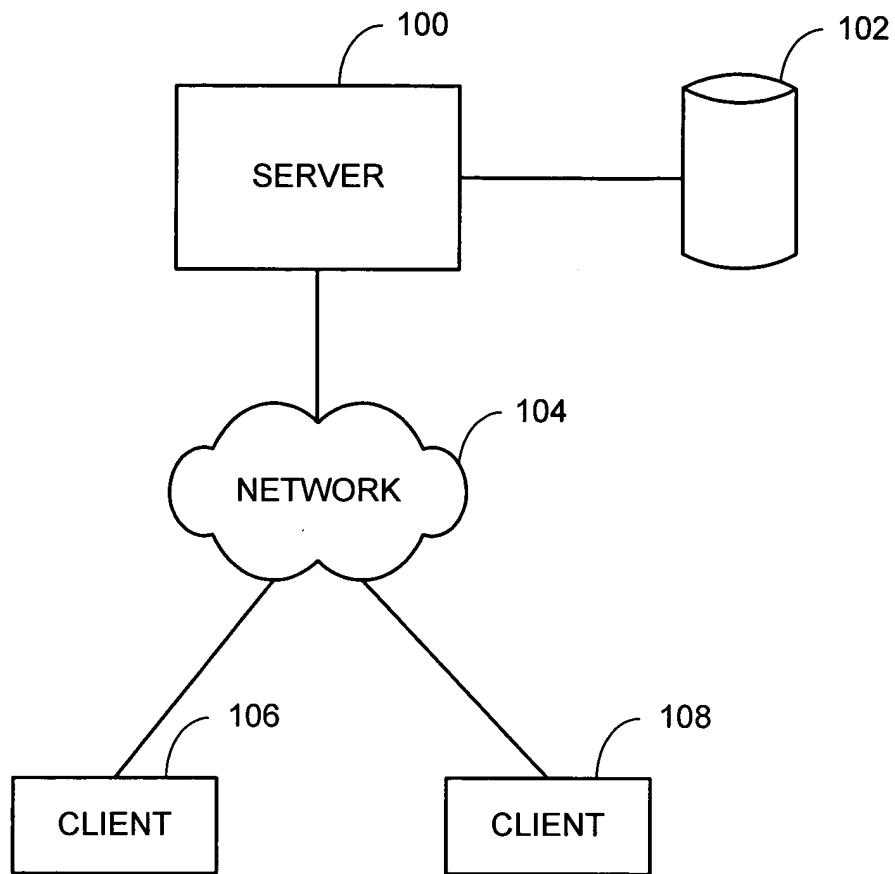


FIG. 9

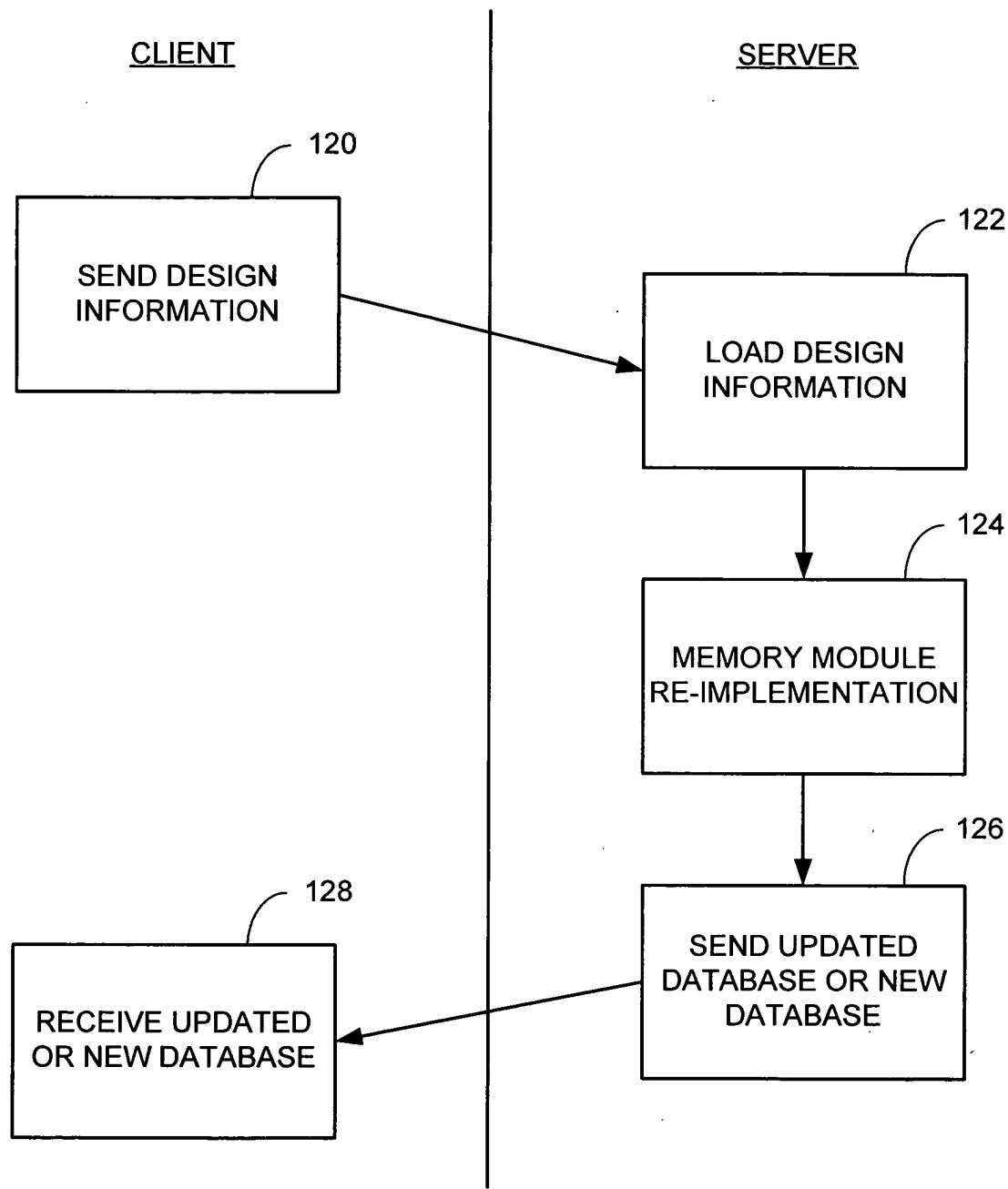


FIG. 10